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# Subthreshold I-V Characteristics of AlGaAs/GaAs MODFETs: The Role of Unintentional Acceptors

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**Development Group** 

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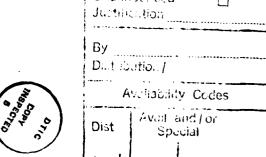
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threshold I-V characteristics in MODFETs (modulation doped field-effect transistors). The									
results of this calculation yield the MODFET equivalent of the MOSFET (metal-oxide semi- conductor field-effect transistor) charge sheet subthreshold model. For typical molecular									
beam epitaxy (MBE) grown structure, the subthreshold current may differ by two-orders of magnitude for a given gate voltage V <sub>g</sub> and drain-to-source voltage V <sub>ds</sub> as the acceptor doping									
varies from 10 <sup>13</sup> to 10 <sup>15</sup> cm <sup>-3</sup> . For these acceptor doping densities, the gate voltage, for a									
given drain-to-source voltage, needed to maintain a constant subthreshold current, varies by									
only - 0.1 V. If the acceptor density is increased to $10^{17}$ cm <sup>-3</sup> , a large increase (= 0.8 V)									
in the gate voltage is required to maintain a constant subthreshold current. These changes in subthreshold current with acceptor concentration in the bulk GaAs are significant and need									
to be included in an accurate MODFET model.									
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#### I. INTRODUCTION

The dependence of the threshold voltage and radiation response of n-channel AlGaAs/GaAs MODFETs (modulation doped field-effect transistors) on acceptor doping density has been described previously (Refs. 1-3). These analyses have now been extended to describe the dependence of MODFET subthreshold I-V characteristics on acceptor doping density.

The band structure of a typical AlGaAs(n)/GaAs heterojunction with Schottky barrier,  $\phi_m$ , at the gate, and a spacer layer at the interface under bias  $V_g$ , is shown in Fig. 1. In the depletion layer approximation, the donors and acceptors are assumed to be completely ionized in the doped AlGaAs layer d, the spacer layer a, and in the depletion layer W. The doping densities  $N_D$  and  $N_A$  are assumed constant. The quasi-two-dimensional electron eigenstates at the interface are solved for using a triangular potential well, and only the lowest subband is included in the calculation. A delta-function channel charge distribution at the average channel width is assumed. Band bending from the interface at (d + a) to the edge of the depletion region (W + d + a) is the difference of the position of the conduction band relative to the Fermi level in the GaAs far from the interface (Eg/2 +  $\phi_{\rm bulk}$ ) and the Fermi level Ef relative to the bottom of the two-dimensional channel.

Under the restrictions imposed by these assumptions, Poisson's equation may be integrated across the structure to obtain the applied gate voltage as a function of device geometry, doping densities, and channel charge  $\mathbf{n}_{\mathbf{S}}$ :

$$V_g = V_0 + f(n_s)$$
 (1)

where  $V_0$  is the difference between the Schottky barrier height and the sum of the band offset and potential drop across the doped AlGaAs layer due to the ionized donors. The function  $f(n_s)$  may be written as:

$$f(n_s) = (q/\epsilon)(d + a)(N_AW + n_s) + C_0(N_AW + n_s)^{2/3} + (kT/q)ln[exp(n_s/n_s) - 1]$$
(2)

where  $C_0$  is a function of the Planck constant, the carrier effective mass, the elemental charge, and the permittivity of AlGaAs and GaAs, assumed identical.  $C_0$  is equal to  $-1.7 \times 10^{-9}$  V-cm $^{4/3}$ . Similarly, the charge density  $n_c$  is a function of physical constants and the effective carrier mass and is equal to  $-8.4 \times 10^{11}$  cm $^{-2}$ . The quantities q,  $\epsilon$ , k, and T are elemental charge, AlGaAs(GaAs) permittivity (assumed identical), Boltzmann constant, and absolute temperature. A discussion of the dependence of the depletion width W on acceptor density has been given elsewhere (Ref. 1). In section II we describe the mathematical properties of the function  $f(n_s)$ , near threshold. In section II we exploit these properties to derive the subthreshold characteristics of these devices.

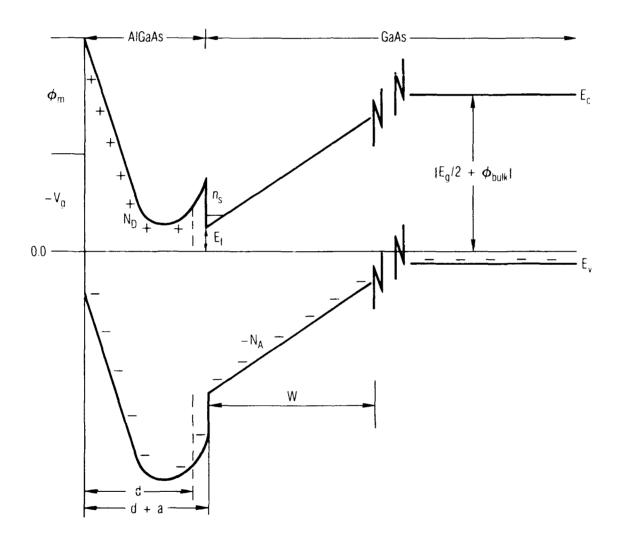


Fig. 1. Band Diagram of a Typical AlGaAs/GaAs MODFET with Schottky Gate, under bias  $\rm V_{\it g}$  .

#### II. MATHEMATICAL PROPERTIES NEAR THRESHOLD

At threshold, the channel density may be defined as equal to the acceptor density  $N_A$  times the average channel width  $z_{av}$ , which may be calculated in the triangular-well approximation using variational wave functions (Refs. 4, 5). This definition of threshold is equivalent to that used for MOSFETs (metal-oxide semiconductor field-effect transistors). Calculation of the threshold carrier density is described elsewhere (Ref. 1). In the subthreshold region, where  $n_S \sim N_A z_{av} \sim N_A W$ , Eq. (2) may be approximated as

$$f(n_s) = (q/\epsilon)(d + a)N_A + C_0(N_AW)^{2/3} + (kT/q)ln(n_s/n_c)$$
 (3)

In Fig. 2,  $f(n_S)$  versus  $n_S$  is shown for two extremes of the acceptor density. The solid curves are the results of Eq. (2), and the dashed curves are the results of Eq. (3).

ine approximation is almost identical to the exact results to channel densities up to  $10^{10}~\rm cm^{-2}$ . The approximate results do not diverge significantly from the exact results until the channel density is above  $10^{11}~\rm cm^{-2}$ . Letting

$$V_{0}' = V_{0} + (q/\epsilon)(d + a)N_{A}W + C_{0}(N_{A}W)^{2/3}$$
 (4)

the applied gate voltage in the subthreshold region,  $V_{\rm gsub}$ , is related to the channel charge in the following way:

$$V_{gsub} = V_0' + (kT/q) \ln(n_s/n_e).$$
 (5)

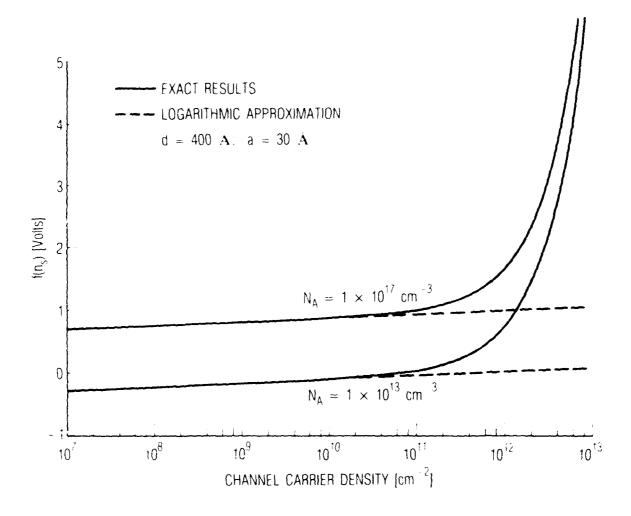


Fig. 2. Function  $f(n_s)$  versus Channel Carrier Density for Two Extremes of Acceptor Density. The solid curves are the exact results of Eq. (2); the dashed lines are the results of the logarithmic approximation, described in text.

#### III. SUBTHRESHOLD I-V CHARACTERISTICS

In the gradual channel approximation, charge control is determined by the effective potential in the channel:

$$V(x) = V_{gsub} - V_{c}(x)$$
 (6)

where  $V_{\bf c}({\bf x})$  is the channel voltage under the gate at point  ${\bf x}$ . Using Eqs. (5) and (6) we solve for the carrier density in the channel:

$$n_s(x) = n_c \exp\{(q/kT)[V_{gsub} - V_0' - V_c(x)]\}$$
 (7)

The form of Eq. (7) allows the source-drain current to be calculated in the usual way (Ref. 6). The result is

$$I_{sub} = kTu(Z/L)n(0)\{1 - exp\{-q(V_D - V_S)/kT\}\}$$
 (8)

where

$$n(0) = n_c \exp[q(V_{gsub} - V_0' - V_S)/kT]$$
 (9)

u = channel mobility

Z = channel width

L = channel length

Equation (8) is the MODFET equivalent of the charge sheet subthreshold current derived by Brews (Ref. 7). In the subthreshold region (i.e., the channel charge in the whole channel is of the order of the threshold carrier density), the source-to-drain voltage is small, and the exponentials containing these terms may be linearized.

The subthreshold current versus the applied gate vortage for various acceptor doping densities is shown in Fig. 3 for the device parameters listed. Source and drain resistances have been ignored. The dashed line is the current produced for a constant channel carrier density of  $10^{11}~\rm cm^{-2}$  throughout the channel. Our subthreshold approximation of  $\Gamma(n_{\rm S})$  diverges from Eq. (2) in this range. Therefore, the subthreshold current calculation is unreliable for this range of carrier densities. Device source-drain currents will depart from the subthreshold currents shown at about 0.1 mA for the device parameters shown. The observed subthreshold current at very large acceptor doping may include a contribution from conduction through the undepleted region of the bulk p-GaAs. This effect has been ignored in our calculation.

At low subthreshold currents, 0.1 - 1 nA, the subthreshold currents are constant with decreasing gate bias. In this region a large change in  $V_{\rm gsub}$  is required to produce a relatively small change in the channel carrier density.

The dependence of the subthreshold current on acceptor density is clearly demonstrated. Even for the relatively low unintentional acceptor doping densities that result from typical molecular beam epitaxy (MBE) growth,  $10^{13} - 10^{15} \text{ cm}^{-3}$ , the subthreshold current will vary by two orders of magnitude over this range for a given applied gate voltage. For acceptor doping densities well above this range, the subthreshold I-V characteristics will shift some 0.8 to 0.9 V as shown.

The subthreshold conductance, in the limit of small drain-source voltage and zero drain-source resistance, is given by the subthreshold current divided by the drain-source voltage and, therefore, has the same dependence on  $V_{\rm DS}$  and  $V_{\rm g}$  as the subthreshold current, as shown in Fig. 3. The subthreshold transconductance, under the same limitations, is given by the subthreshold current divided by kT/q, which also has the same functional form as the subthreshold current.

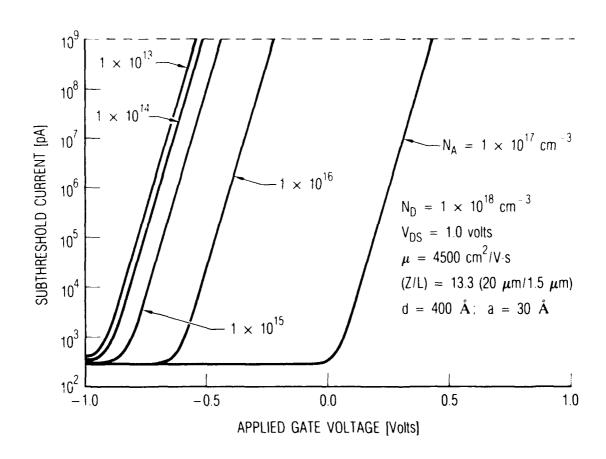


Fig. 3. MODFET Subthreshold Current versus Applied Gate Voltage.

#### IV. SUMMARY

We have developed a triangular-well, one-subband depletion layer model to describe the dependence of MODFET subthreshold I-V characteristics on AlGaAs and GaAs doping densities and device geometry. The description of the role of acceptor doping on I-V characteristics is important for an accurate description of device performance, especially when MODFETs are used in complementary devices. As has been pointed out previously (Ref. 8), intentional doping of the GaAs may be used to tailor device characteristics for enhanced device performance.

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